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Docket No.: 300.1155

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Katsuya FUKASE, et al.

Serial No. 10/822,825

Group Art Unit:

Confirmation No.

Filed: April 13, 2004

Examiner:

For: PROCESS FOR MAKING CIRCUIT BOARD OR LEAD FRAME

PRELIMINARY AMENDMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Before examination of the above-identified application, please amend the application as follows:

AMENDMENTS TO THE DRAWINGS:

The drawings are amended as described below by presenting replacement figures as attached hereto, the changes further being shown by red-ink changes in a copy set of the drawings as originally filed herein.

In sheet 1/11 of Figs. 1(a)-1(d), correct the spelling of "PAIOR" to --PRIOR--.

In sheet 5/11, Figs. 5(e) and 5(f), extend the respective boundary lines between intermediate layer 2 and upper and lower layers 20 and cross hatch the resulting sections 12c as labeled in Fig. 5(e) and as newly labeled in Fig. 5(f).

In Figs. 11(e) and 11(f), extend the respective boundary lines between intermediate layer 102 and upper and lower layers 104 and cross hatch the resulting sections 112c as labeled in Fig. 11(e) (and as Fig. 11(e) has been further amended to include a second such label 112c and in Fig. 11(f), wherein "102c" has been changed to --112c-- and a second such label --112c-- has been added in Fig. 11(f).